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speaker are gathered by a microphone 5 to produce a microphone input voice signal C. The microphone input voice signal C is inputted to the transmitting side attenuation section 2 and the transmitting side control section 3b. transmitting side control section 3b compares a level of the microphone input voice signal C and that of the speaker output voice signal B to detect a difference therebetween. Devendent on the difference thus detected, the transmitting side control section 3b controlls an amount of attenuation in the transmitting side attenuation section 2. transmilling side attenuation section 2 produces signal Attenuated side attenuation section 2, the transmitting transmitted to the unillustrated i s communication line.

Next, referring to Figs. 3 and 4, detailed description is made about the receiving side control section 3a illustrated in Fig 2.

Fig. 3 is a block diagram for showing an internal constitution of the receiving side control section 3a.

In Fig. 3, the receiving voice signal A is inputted to a receiving side signal power estimation section 32. On the other hand, the transmitting voice signal D is inputted to a transmitting side signal delay buffer 34. An output of the transmitting side signal delay buffer 34 is inputted to a transmitting side signal power estimation section 33. An output of the receiving side signal power estimation section 32 and an output of the transmitting side

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inputted to the receiving side attenuation section 1 and the eceiving side control section 3a. In the receiving side control section 3a illustrated in Fig. 3, the receiving voice signal A is inputted to a receiving side signal power estimation section 32. The receiving side signal power astimation section 32 estimates a signal power of the receiving voice signal A. Thus estimated signal power of the receiving voice signal A is outputted to the comparator On the other hand, the transmitting voice signal D outputted from the transmitting side attenuation section 2 in Fig. 2 is inputted to the transmitting side signal delay buffer 34 of the receiving side control section 3a to be The delayed transmitted voice signal D delayed therein. is thereafter outputted to the transmitting side signal power estimation section 33. The transmitting side signal power estimation section 33 estimates a signal power of the transmitting voice signal D. The estimated signal power of voice signal D is outputted to the comparator 31.

power of the receiving voice signal A and that of the transmitted voice signal D. In this comparison by the comparator 31, the delayed amount of the transmitting voice signal D by the transmitting voice signal delay buffer 34 is adjusted to be equal to a delayed amount due to the communication line. In other words, the delayed amount of the transmitting voice signal delay buffer 34 is adjusted to be equal to the transmitting side signal delay buffer 34 is adjusted to be equal to the delayed signal delay buffer 34 is adjusted to be equal to the delayed

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